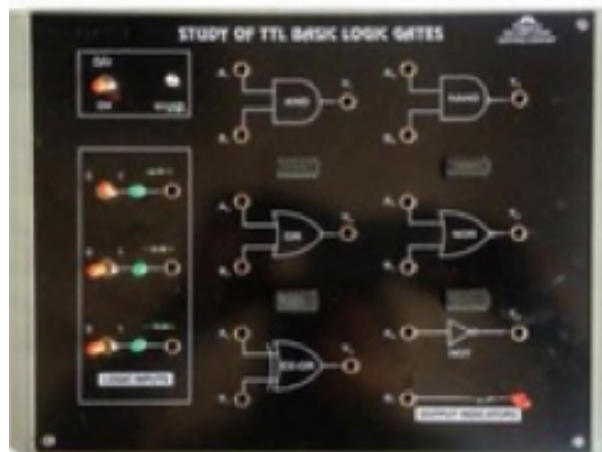


Study of Logic Gates using TTL ICs (6 in 1)

Product code: GE372

Objective: To verify the truth table for TTL AND, OR, NOT, NAND, NOR, EX-OR & EX-NOR gates.



Features: instrument comprises of 5V/150mA DC Regulated Power Supply for logic 1 & logic 0. Circuit Diagram Printed on Glass Epoxy PCB & all important ICs and test points are brought out on front panel.